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		Application Number	10/727,910
		Filing Date	December 4, 2003
		First Named Inventor	Ju Yan Pan
		Art Unit	2124
		Examiner Name	---
Sheet 1 of 1	Attorney Docket Number	121812.00003	

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
FA		Berrou, et al., "Near Shannon Limit Error-Correcting Coding and Decoding: Turbo-Codes," in Proceedings of ICC'93, Geneva, Switzerland, ppg. 1064-1070, May 23-25 1993.	
FA		Warren J. Gross and P. Glenn Gulak, "Simplified MAP Algorithm Suitable for Implementation of Turbo Decoders," Department of Electrical and Computer Engineering, University of Toronto, Toronto, Canada.	
FA		Chass, et al., "Efficient Software Implementation of the Max-Log-MAP Turbo Decoder on the StarCore SC140 DSP," Motorola Semiconductor Israel Ltd, Shenkar 1, Herzelia, Israel.	
FA		Patrick Robertson and Peter Hoeher, "Optimal and Sub-Optimal Maximum a Posteriori Algorithms Suitable for Turbo Decoding," European Transactions on Telecommunications, vol. 8, no. 2, March-April 1997, pp. 119-125.	
FA		EG TS 25:212 V3.3.0 (2000-06), "3rd Generation Partnership Project; Technical Specification Group Radio Access Network; Multiplexing and channel coding (FDD) (Release 1999) pgs. 1-62.	
FA		IEEE Transactions on Information Theory, March 1974, pgs. 284-287.	

Examiner Signature	/Fritz Alphonse/	Date Considered	06/25/2006
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.
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